

Model Name: GA-H67MA-USB3-B3

Revision 1.0

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI EXPRESS*1x2 SLOT
17	ITE 8728 LPC IO
18	COM,KB_USB,USB_ESATA,-PROCHOT
19	HWM,FAN CTRL
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	ALC889
23	REAR AUDIO JACK
24	REALTEK RTL8111E
25	HDMI/DVI/DP
26	USB3.0- EJ168
27	DISCRETE POWER

SHEET

TITLE

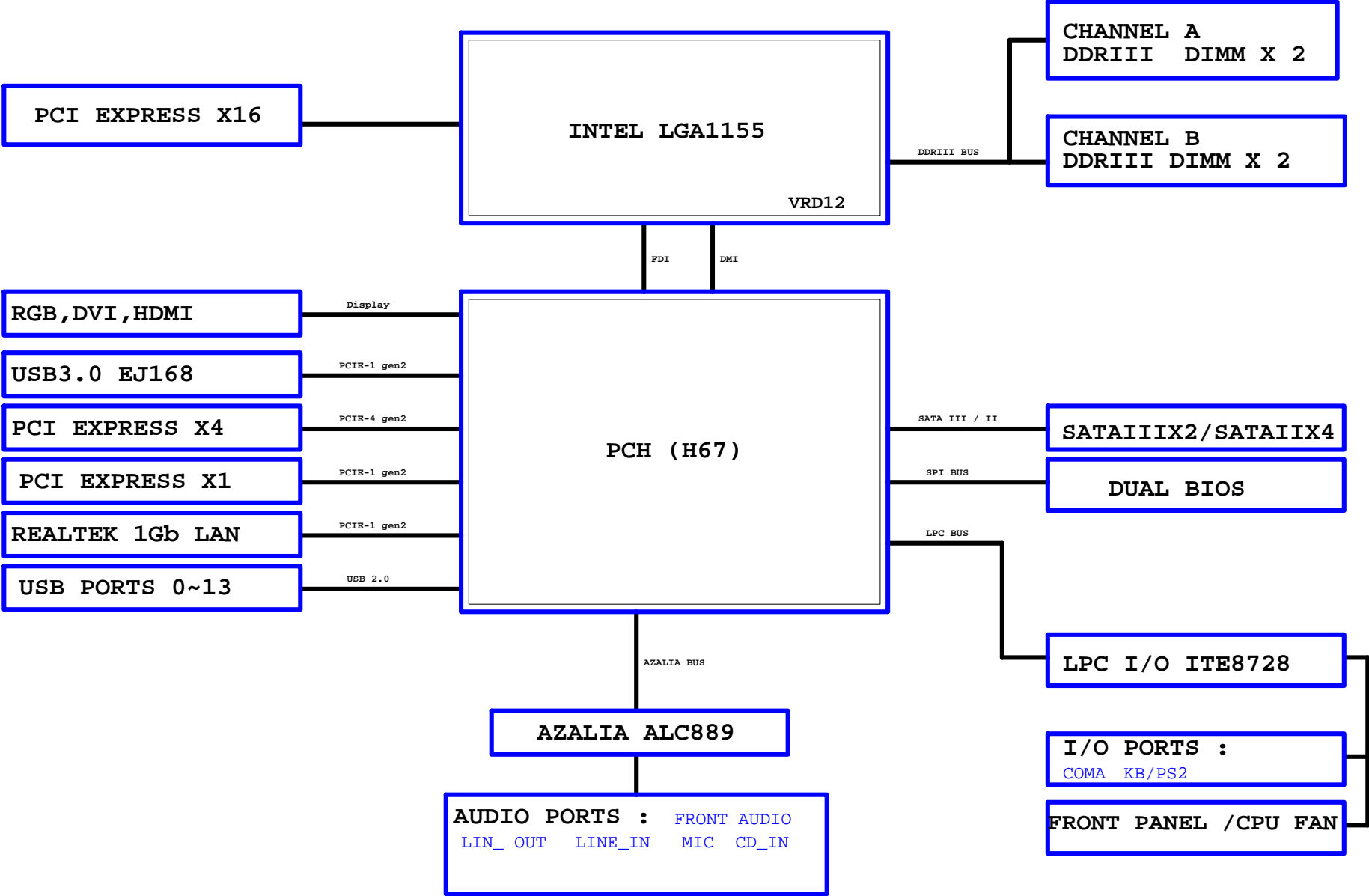
28	ATX,TPM
29	ISL95870_CPU_VTT
30	VCORE ISL6364_1
31	VCORE ISL6364_2
32	VCORE ISL6364_3

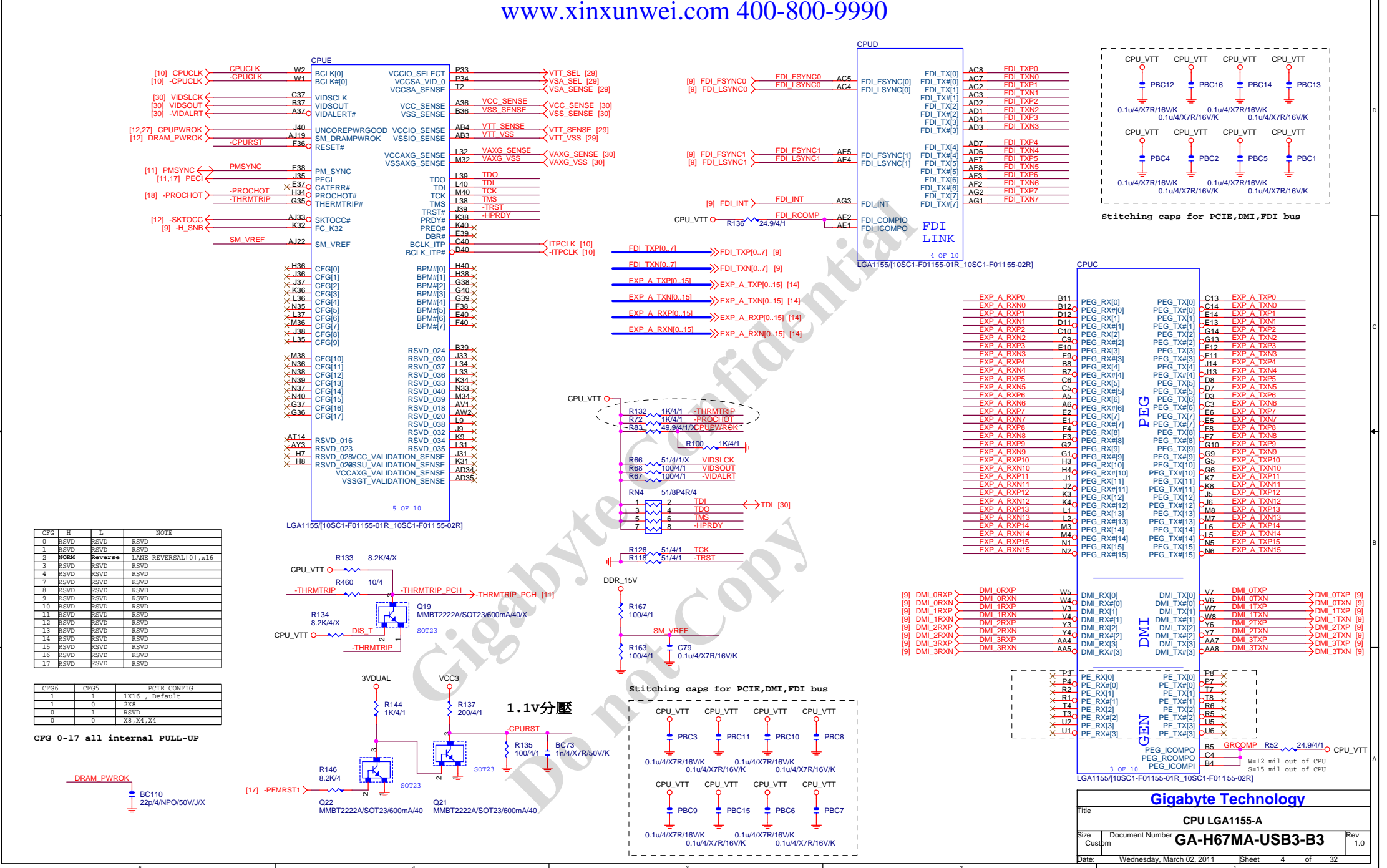
Gigabyte Technology

Cover Sheet

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BLOCK DIAGRAM





CPUA

MAAA0	AV27	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AV23	SA_MA[3]			
MAAA4	AV23	SA_MA[3]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[4]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[5]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MA[6]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MA[7]	AJ2	MDA4	
MAAA9	AT22	SA_MA[8]	SA_DQ[5]	AJ1	MDA5
MAAA10	AV28	SA_MA[9]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[10]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MA[11]			
MAAA13	AW32	SA_MA[12]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[13]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[14]			
		SA_MA[15]			
[7] -SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
[7] -SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
[7] -SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
			SA_DQ[11]	AR4	MDA12
[7] SBAA0	AY29	SA_BS[0]	SA_DQ[12]	AN2	MDA11
[7] SBAA1	AW28	SA_BS[1]	SA_DQ[13]	AN3	MDA13
[7] SBAA2	AV20	SA_BS[2]	SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
[7] -CSA0	AY29	SA_CS#	SA_DQS[2]	AW4	DQSA2
[7] -CSA1	AV32	SA_CS#	SA_DQS[2]	AW4	-DQSA2
[7] -CSA2	AW30	SA_CS#	SA_DQS[2]		
[7] -CSA3	AU33	SA_CS#			
[7] CKEA0	AV19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
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[7] CKEA2	AU18	SA_CKE[2]	SA_DQ[18]	AV5	MDA18
[7] CKEA3	AV18	SA_CKE[3]	SA_DQ[19]	AW5	MDA19
			SA_DQ[20]	AU2	MDA20
			SA_DQ[21]	AU3	MDA21
			SA_DQ[22]	AY5	MDA22
			SA_DQ[23]	AY5	MDA23
			SA_DQS[3]	AV8	DQSA3
			SA_DQS[3]	AW8	-DQSA3
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[7] DCLKA1	AU24	SA_CK[1]	SA_DQ[26]	AU9	MDA26
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[7] DCLKA2	AW27	SA_CK[2]	SA_DQ[28]	AV7	MDA28
[7] -DCLKA2	AY27	SA_CK#	SA_DQ[29]	AW7	MDA29
[7] DCLKA3	AV26	SA_CK[3]	SA_DQ[30]	AW9	MDA30
[7] -DCLKA3	AW26	SA_CK#	SA_DQ[31]	AY9	MDA31
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			SA_DQS[4]	AV36	-DQSA4
			SA_DQ[32]	AU35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AU39	MDA34
			SA_DQ[35]	AU36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AU38	MDA38
			SA_DQ[39]	AU37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN39	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
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DDR_0

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LGA1155[10SC1-F01155-01R_10SC1-F01155-02R]

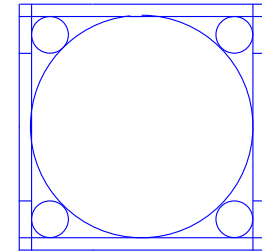
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MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AN18	SB_MA[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]			
MAAB13	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
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MAAB15	AV16	SB_MA[15]			
			SB_DQ[8]	AL7	MDB8
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			SB_DQ[12]	AL6	MDB12
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			SB_DQ[21]	AP9	MDB21
			SB_DQ[22]	AP9	MDB22
			SB_DQ[23]	AR9	MDB23
			SB_DQS[3]	AN13	DQSB3
			SB_DQS[3]	AN12	-DQSB3
[8] DCLKB0	AL21	SB_CK[0]	SB_DQ[24]	AM12	MDB24
[8] -DCLKB0	AL22	SB_CK#	SB_DQ[25]	AM13	MDB25
[8] DCLKB1	AK20	SB_CK[1]	SB_DQ[26]	AR13	MDB26
[8] -DCLKB1	AK20	SB_CK#	SB_DQ[27]	AR13	MDB27
[8] DCLKB2	AM22	SB_CK[2]	SB_DQ[28]	AL12	MDB28
[8] -DCLKB2	AM22	SB_CK#	SB_DQ[29]	AL13	MDB29
[8] DCLKB3	AP21	SB_CK[3]	SB_DQ[30]	AR12	MDB30
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			SB_DQ[42]	AP32	MDB40
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			SB_DQ[44]	AP35	MDB42
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			SB_DQ[48]	AR35	MDB46
			SB_DQ[49]	AR34	MDB47
			SB_DQS[6]	AL33	DQSB6
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			SB_DQ[50]	AL35	MDB50
			SB_DQ[51]	AL32	MDB51
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			SB_DQ[62]	AE33	MDB62
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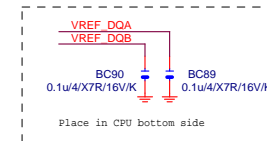
CR
CPU RETENTIONX

Need check the new CPU ME

CPU_P

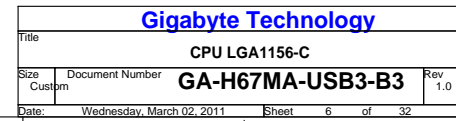


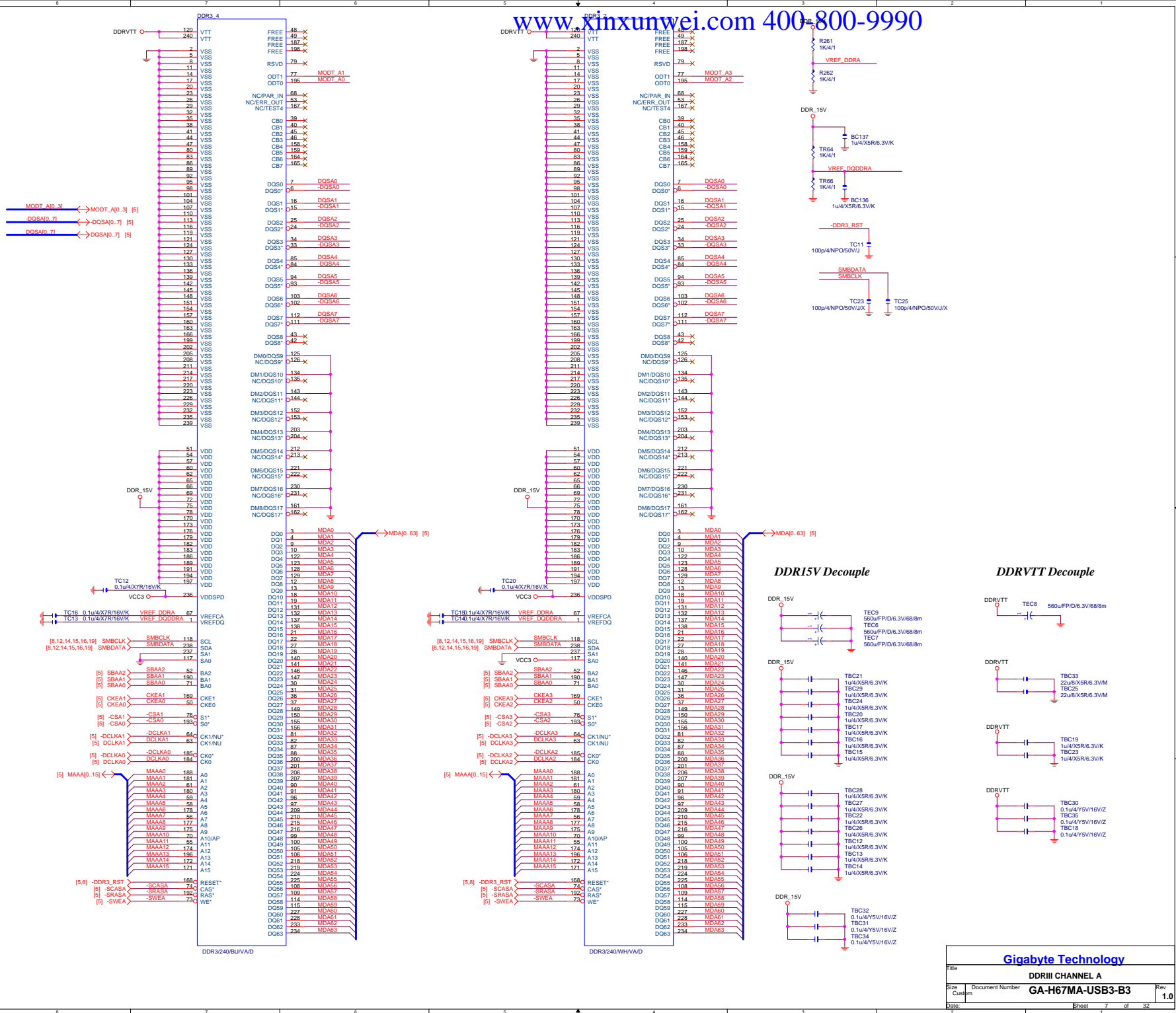
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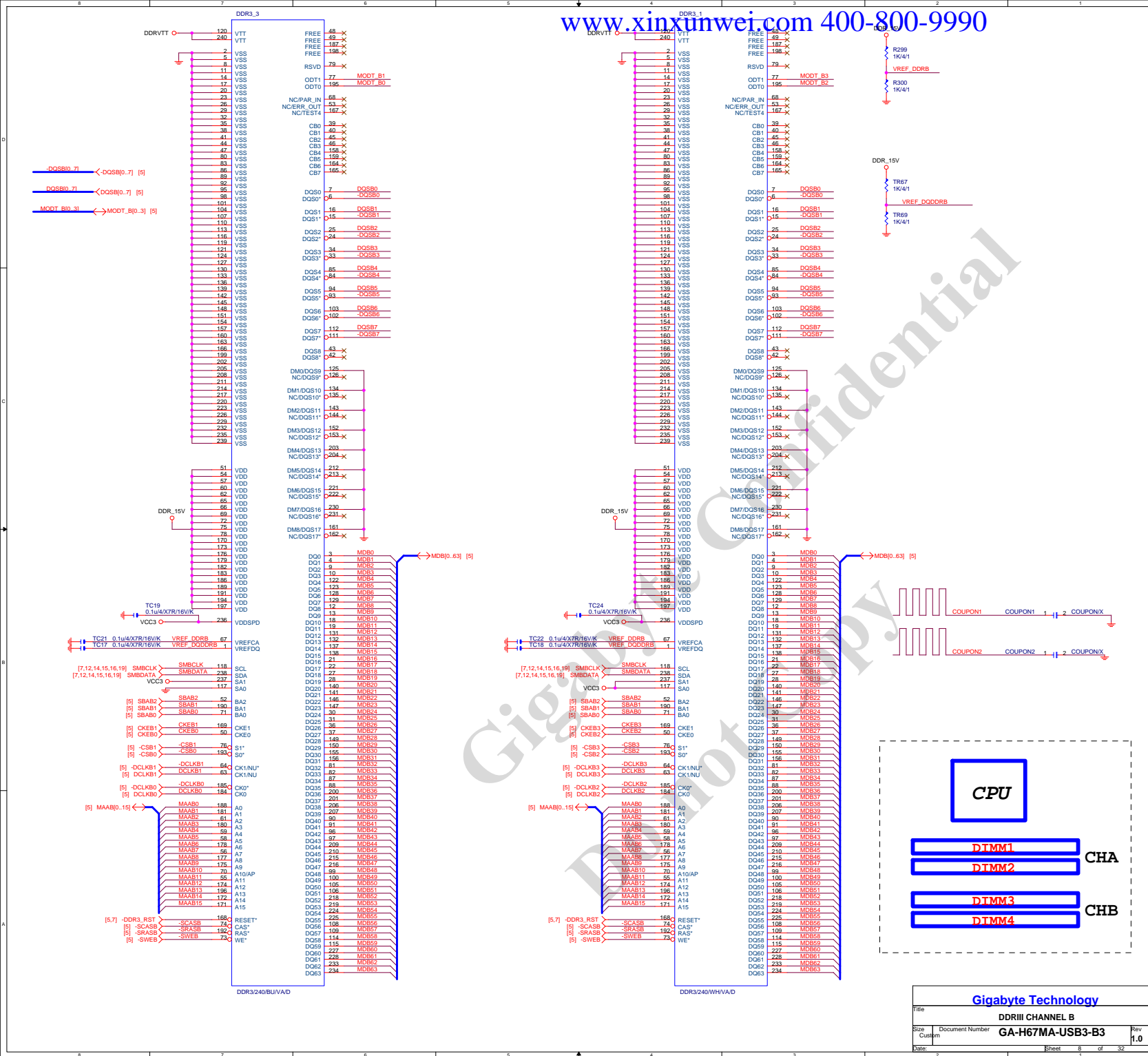


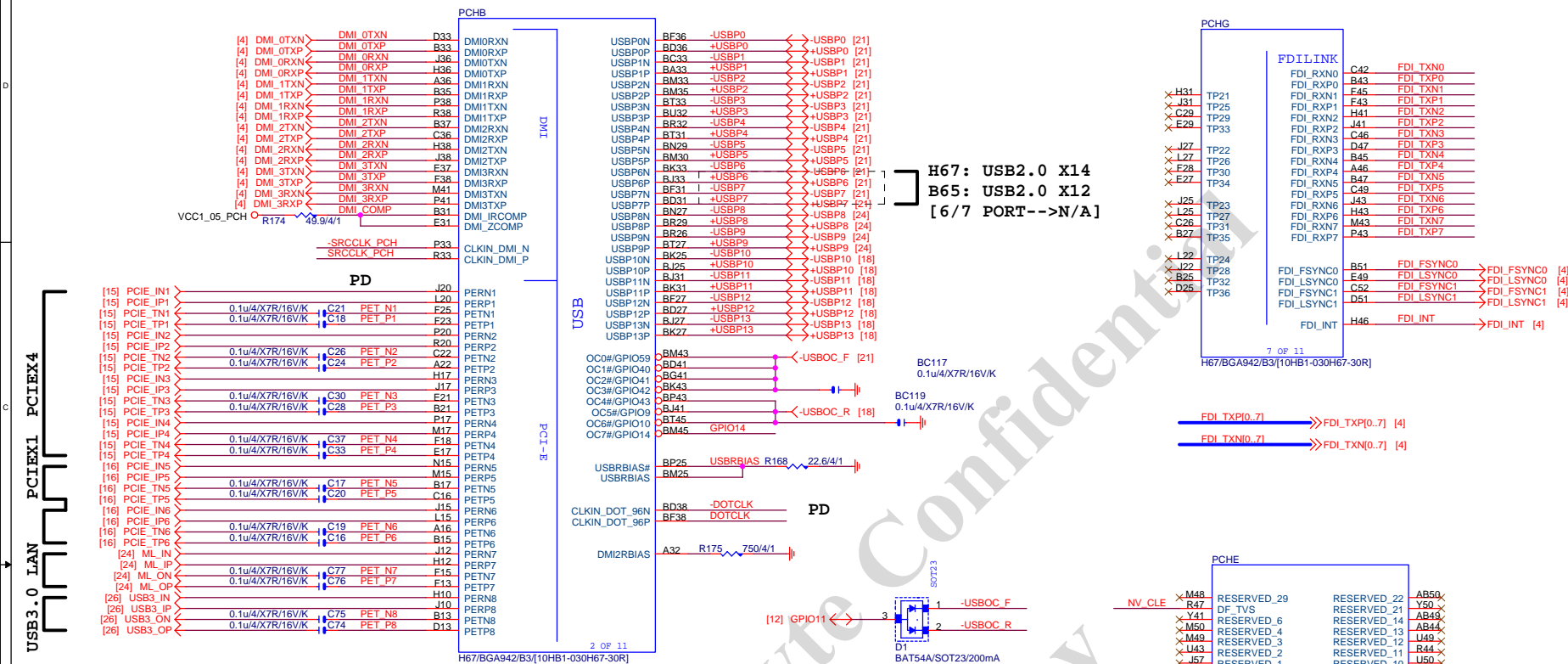
Gigabyte Technology

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Size				Document Number	
Custom				GA-H67MA-USB3-B3	
Date:				Wednesday, March 02, 2011	Rev 1.0
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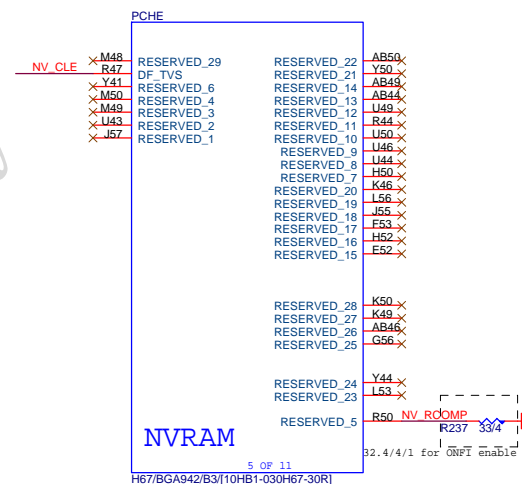
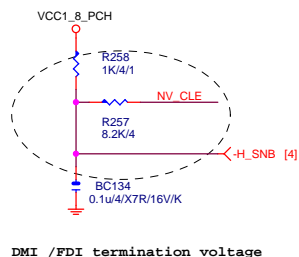
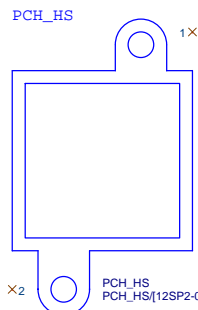
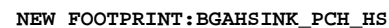
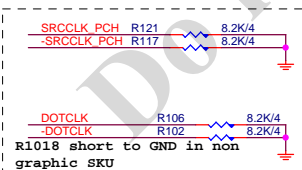


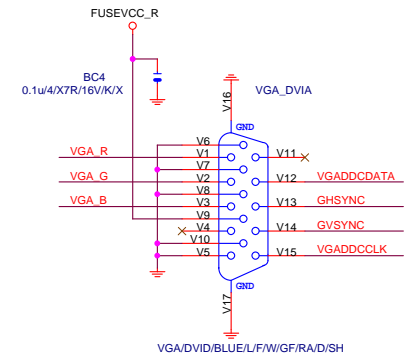
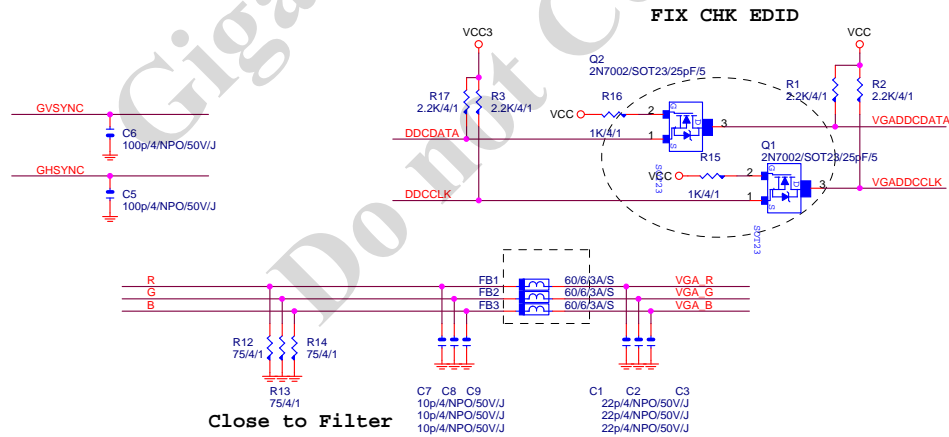
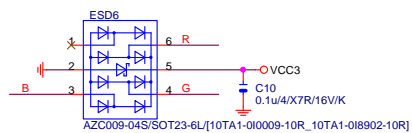
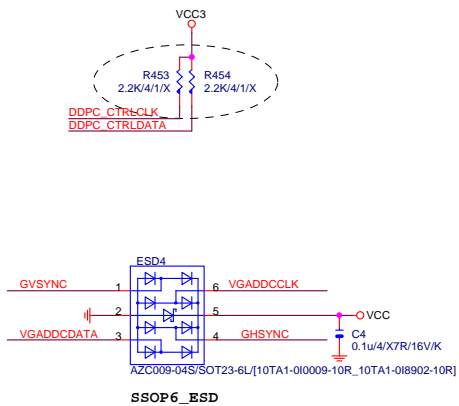
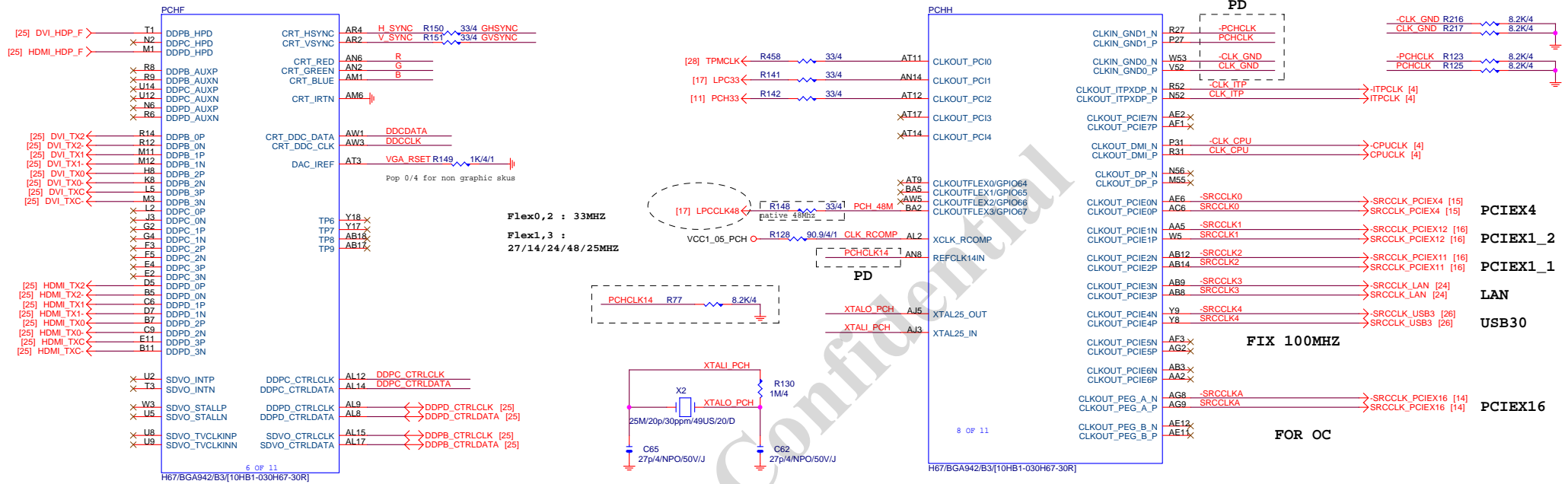




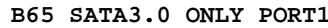
FOR FUSE SHORT GPIO

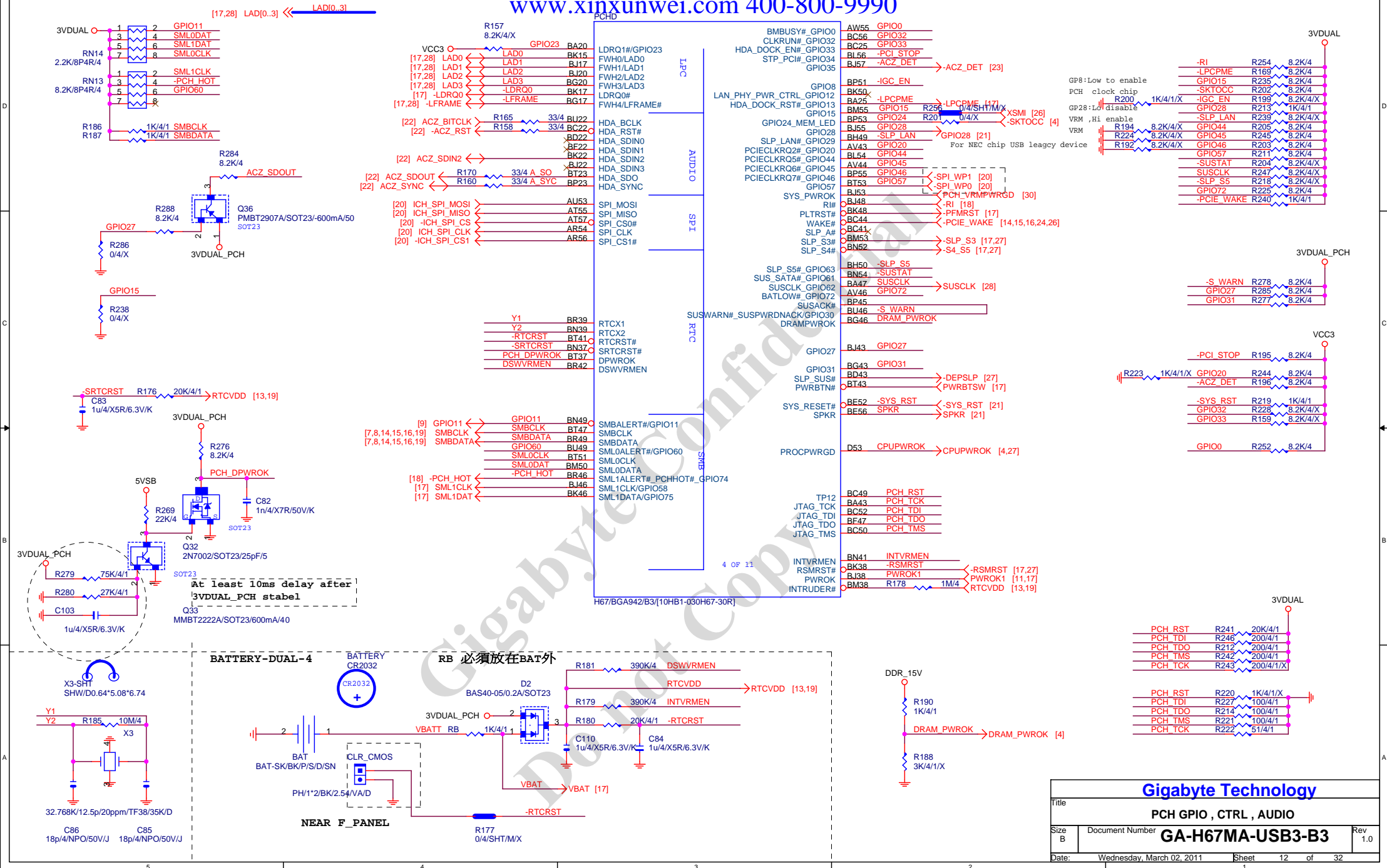
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OC0#	USB0,1(F_USB1)
OC1#	USB2,3(F_USB2)
OC2#	USB4,5(F_USB3)
OC3#	USB6,7(F_USB4)
OC4#	USB8,9(USB_LAN)
OC5#	USB10~11(USB30_20)
OC6#	USB12~13(KB_USB)
OC7#	GPIO14

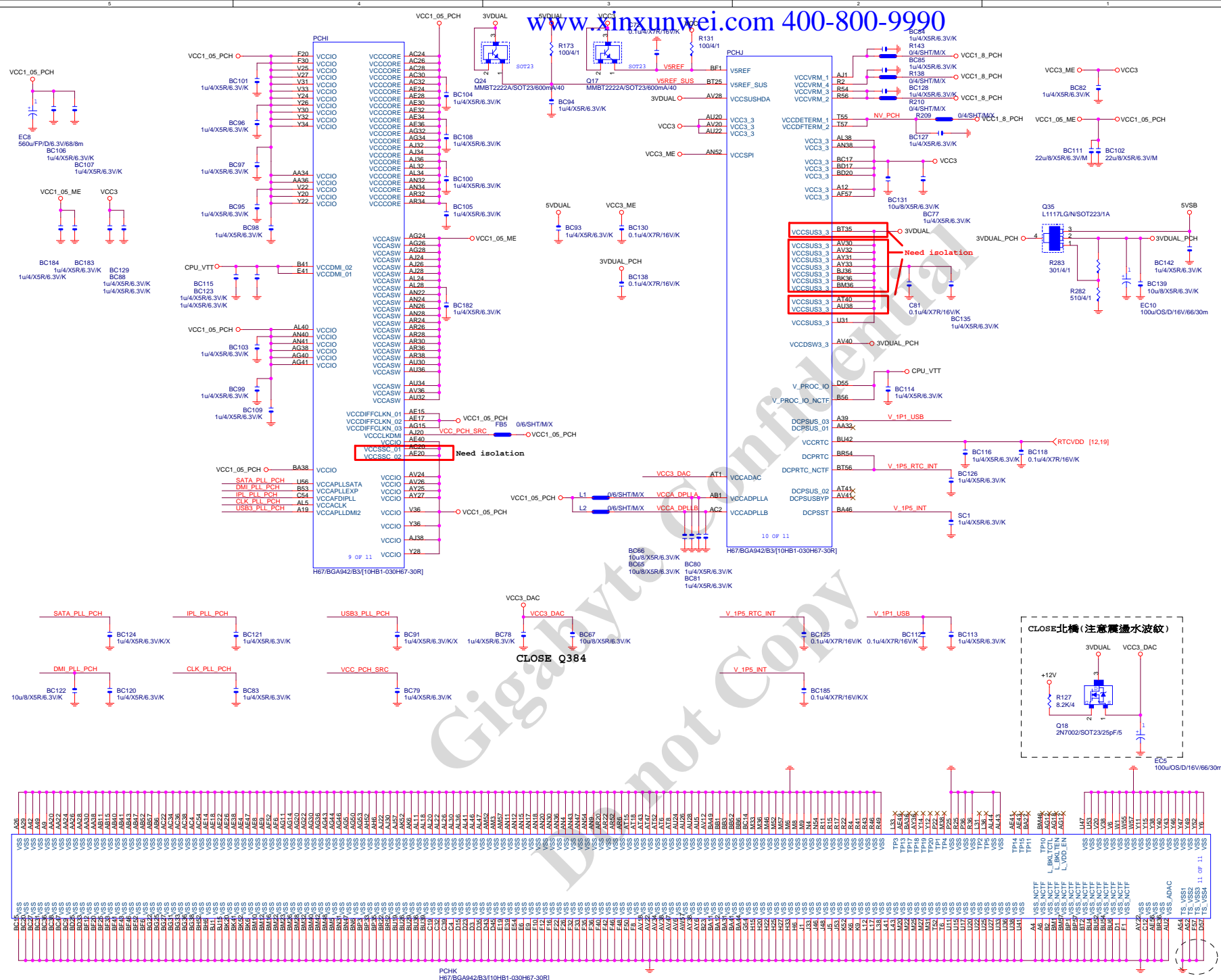


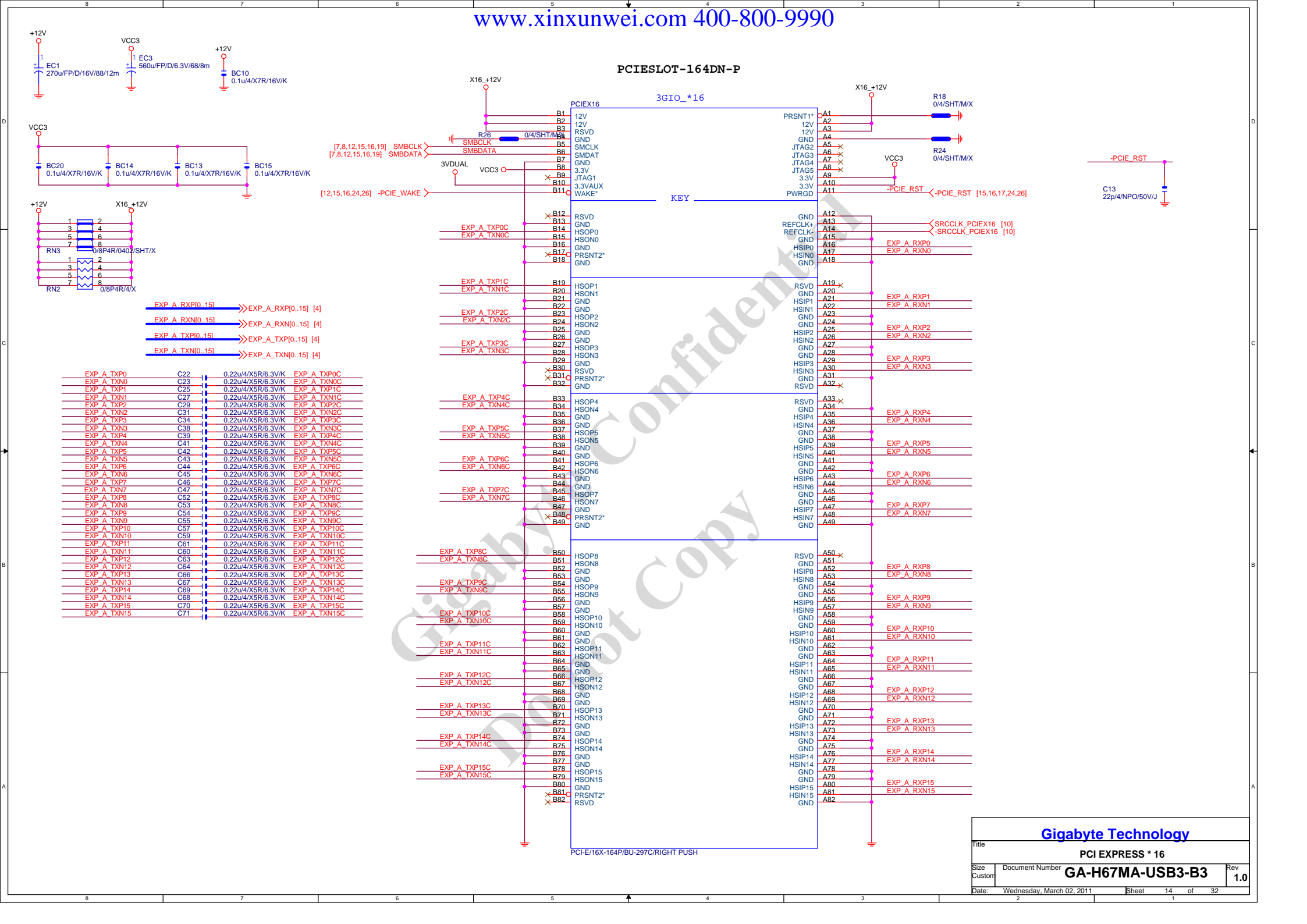


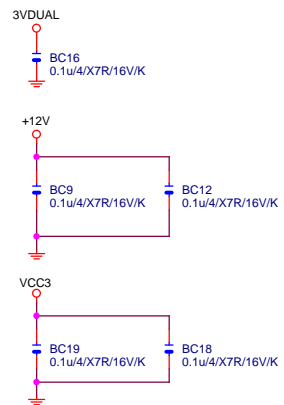
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PCH DISPLAY, CLK BUFFER			
GA-H67MA-USB3-B3			
Title	Document Number	Rev	1.0
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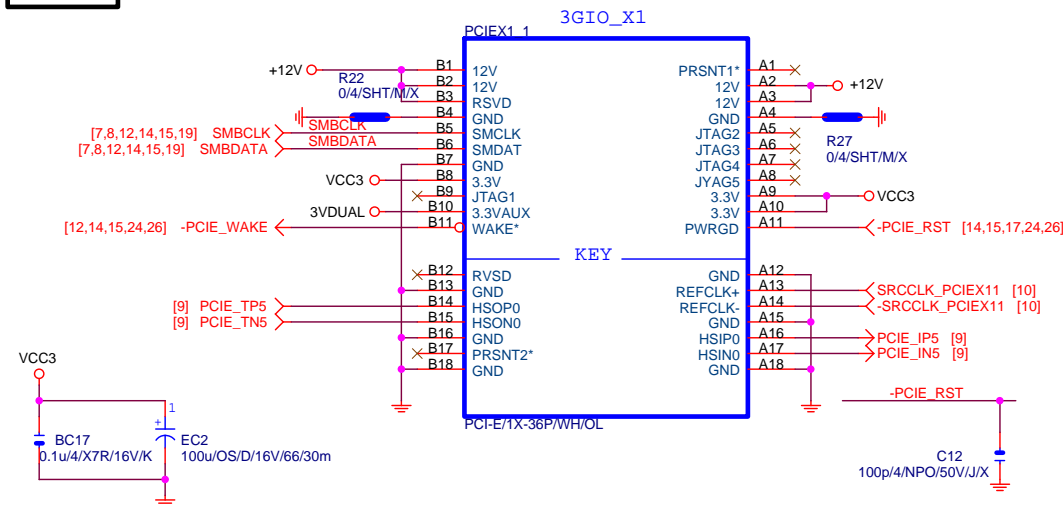








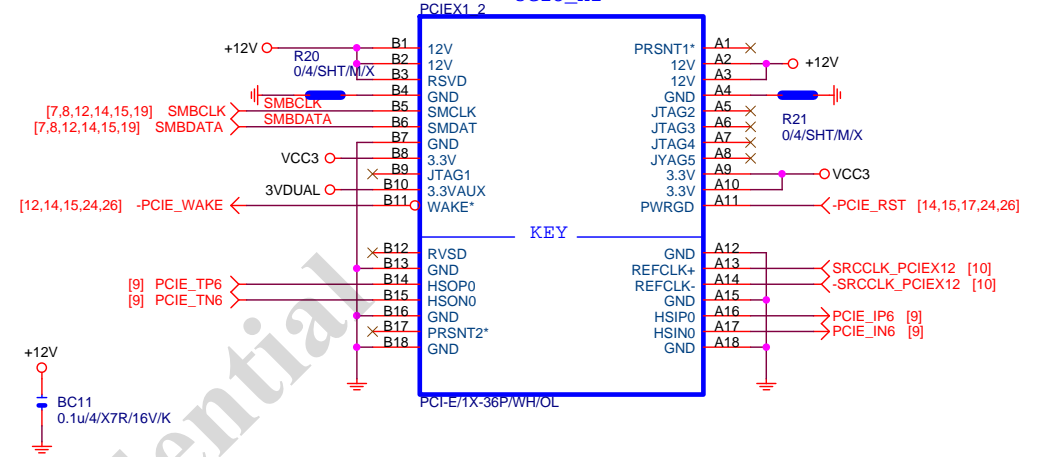
PCIEX1



CLK GEN CK505

N/A

3GIO_X1



Gigabyte Technology

Title		
PCIEX1,X2		
Size	Document Number	Rev
Custom	GA-H67MA-USB3-B3	1.0
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ITE 8728F (GB)

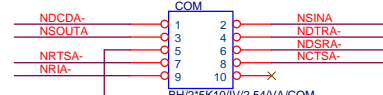
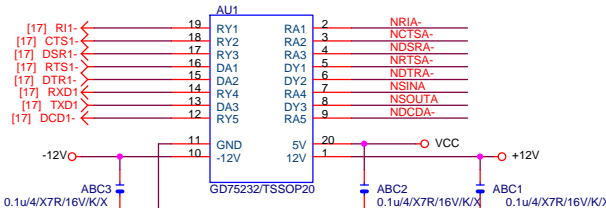
Pin List:

Pin	Signal
PIN121	VCORE_EN# / PCH_C0
PIN120	VLDT_EN# / PCH_D0
PIN19	ATXPG
PIN31	PCH_C1
PIN53	SST/AMDTSI_D/MTRB# / PCH_D1
PIN55	PECI/AMDTSI_C/DRV#
PIN66	SYS_3VSB
PIN70	GP47
PIN95	VIN2 (VCC5)
PIN96	VIN1 (VCC12)
PIN97	VIN1/VDIMM_STR (1.5V)
PIN98	VIN0/VCORE (1.1V) / NC

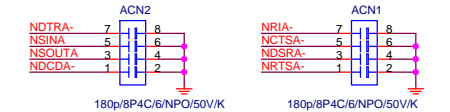
Key components and connections:

- CPU_FAN:** Includes pins for FAN01, FANPWM1, FAN02, FANPWM2, FAN03, FANPWM3, FAN04, FANPWM4, FAN05, FANPWM5, FAN06, FANPWM6, FAN07, FANPWM7, FAN08, FANPWM8, FAN09, FANPWM9, FAN10, FANPWM10, FAN11, FANPWM11, FAN12, FANPWM12, FAN13, FANPWM13, FAN14, FANPWM14, FAN15, FANPWM15, FAN16, FANPWM16, FAN17, FANPWM17, FAN18, FANPWM18, FAN19, FANPWM19, FAN20, FANPWM20, FAN21, FANPWM21, FAN22, FANPWM22, FAN23, FANPWM23, FAN24, FANPWM24, FAN25, FANPWM25, FAN26, FANPWM26, FAN27, FANPWM27, FAN28, FANPWM28, FAN29, FANPWM29, FAN30, FANPWM30, FAN31, FANPWM31, FAN32, FANPWM32, FAN33, FANPWM33, FAN34, FANPWM34, FAN35, FANPWM35, FAN36, FANPWM36, FAN37, FANPWM37, FAN38, FANPWM38, FAN39, FANPWM39, FAN40, FANPWM40, FAN41, FANPWM41, FAN42, FANPWM42, FAN43, FANPWM43, FAN44, FANPWM44, FAN45, FANPWM45, FAN46, FANPWM46, FAN47, FANPWM47, FAN48, FANPWM48, FAN49, FANPWM49, FAN50, FANPWM50, FAN51, FANPWM51, FAN52, FANPWM52, FAN53, FANPWM53, FAN54, FANPWM54, FAN55, FANPWM55, FAN56, FANPWM56, FAN57, FANPWM57, FAN58, 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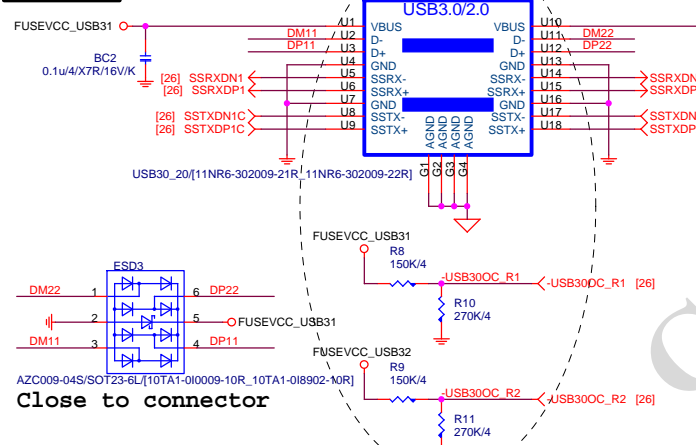
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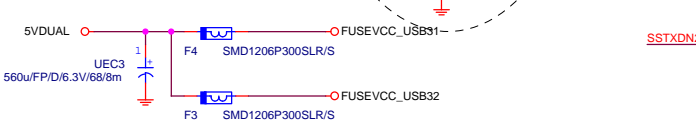
11NH3-000205-Y1R/Y2R



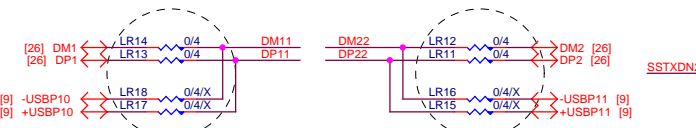
USB30_20



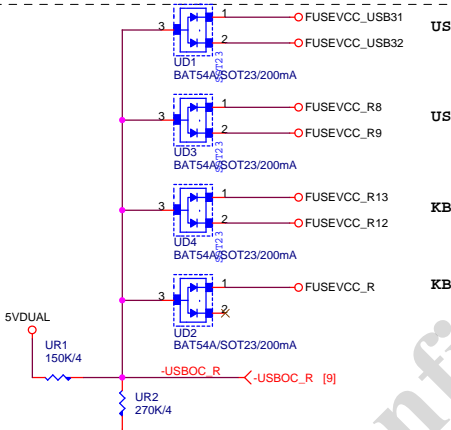
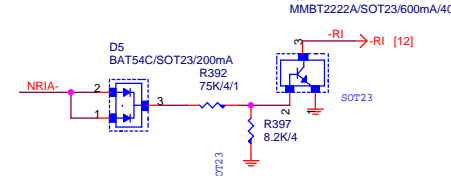
Close to connector



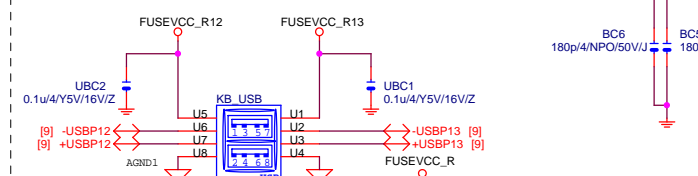
CLOSE USB30_LAN



COM RI



KB_USB



USB30_20

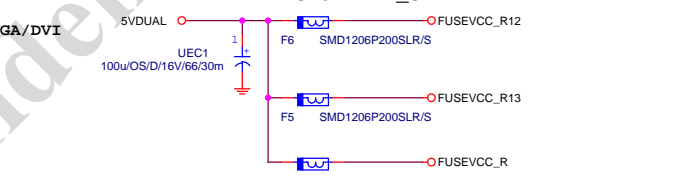
USB_LAN

KB/2USB/PC99 (DUAL)/RED/RA/GF:NEW RED

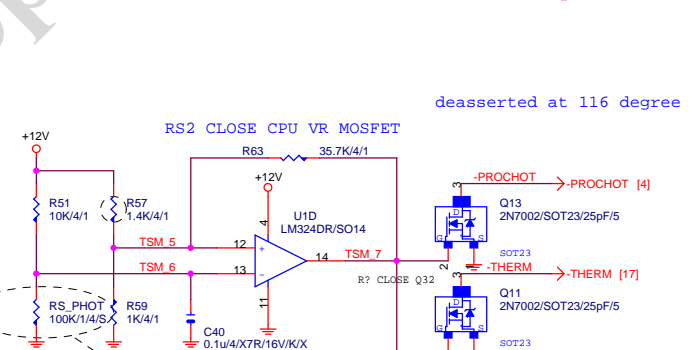
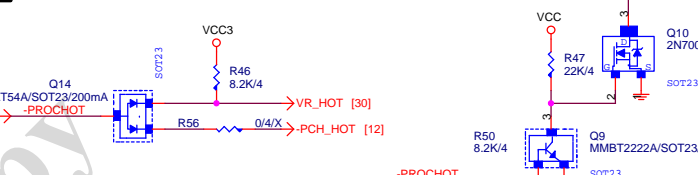
KB_USB

KB/VGA/DVI

CLOSE KB_USB



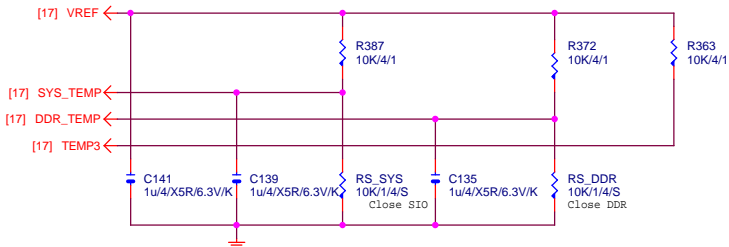
-PROHOT



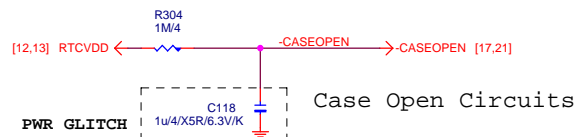
CLOSE PWM HOT MOSFET

Gigabyte Technology			
Title			
COM,-RI,KB_USB,USB_ESATA,-PROHOT			
Size	Document Number	Rev	
Custom	GA-H67MA-USB3-B3	1.0	
Date:	Wednesday, March 02, 2011	Sheet	18 of 32

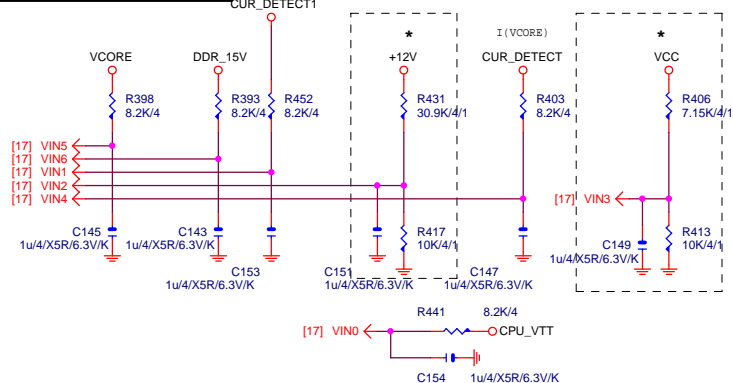
TEMP H/W MONITOR



CASE OPEN

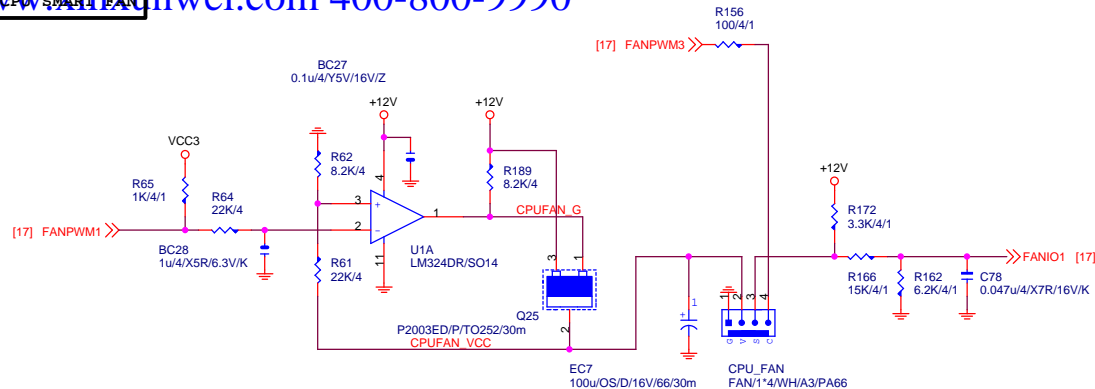


VOLTAGE-- H/W MONITOR

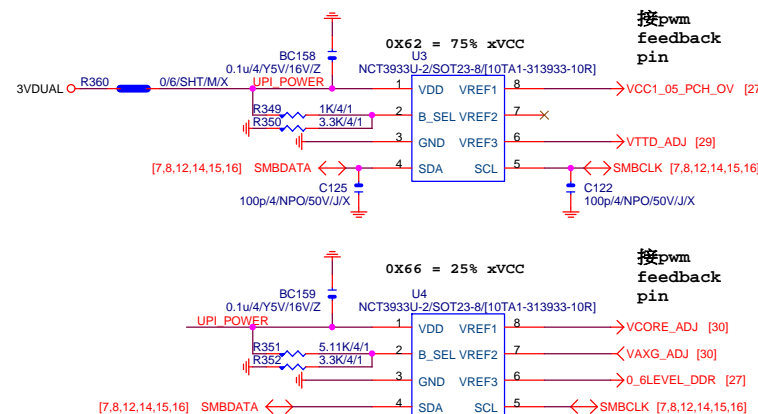
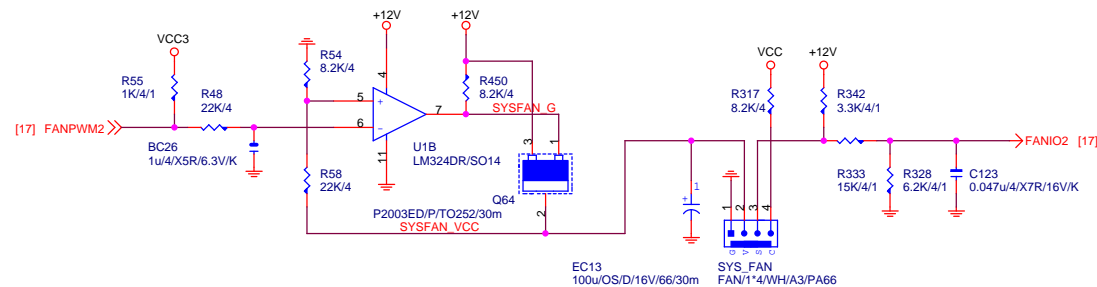


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CPU SMART FAN

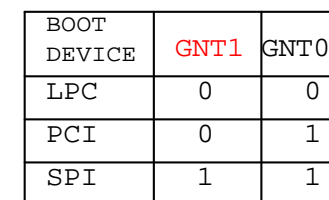


SYS SMART FAN



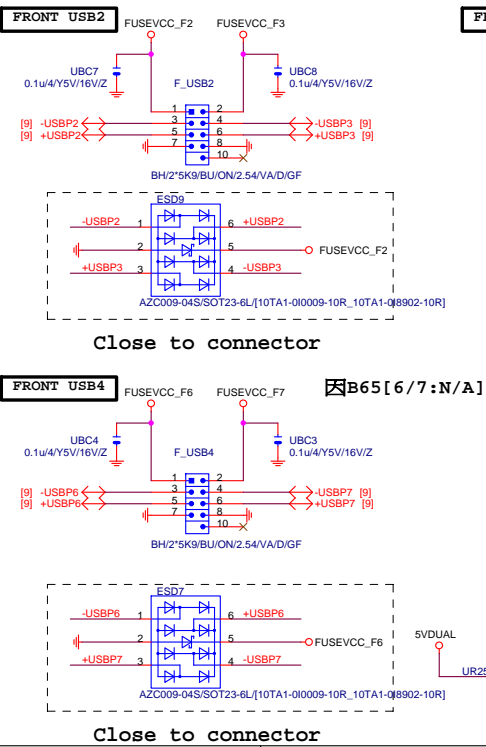
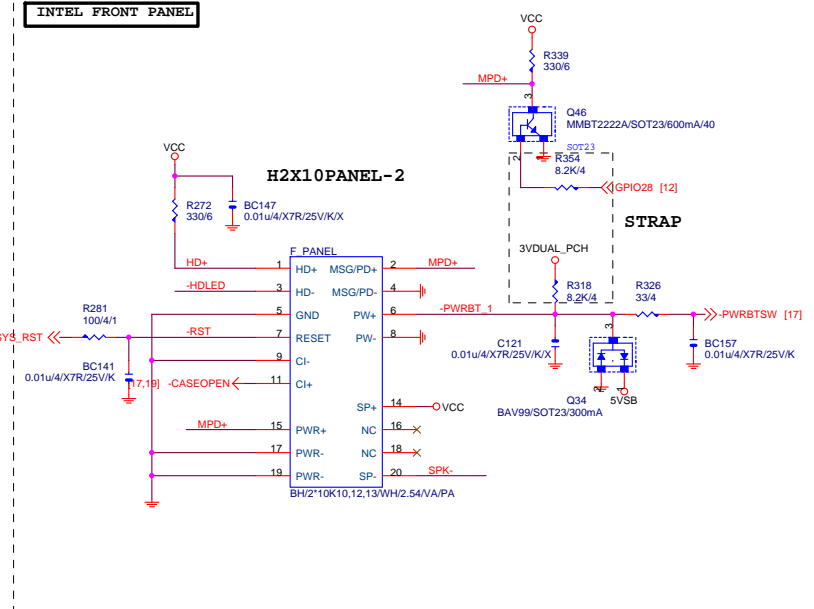
Gigabyte Technology

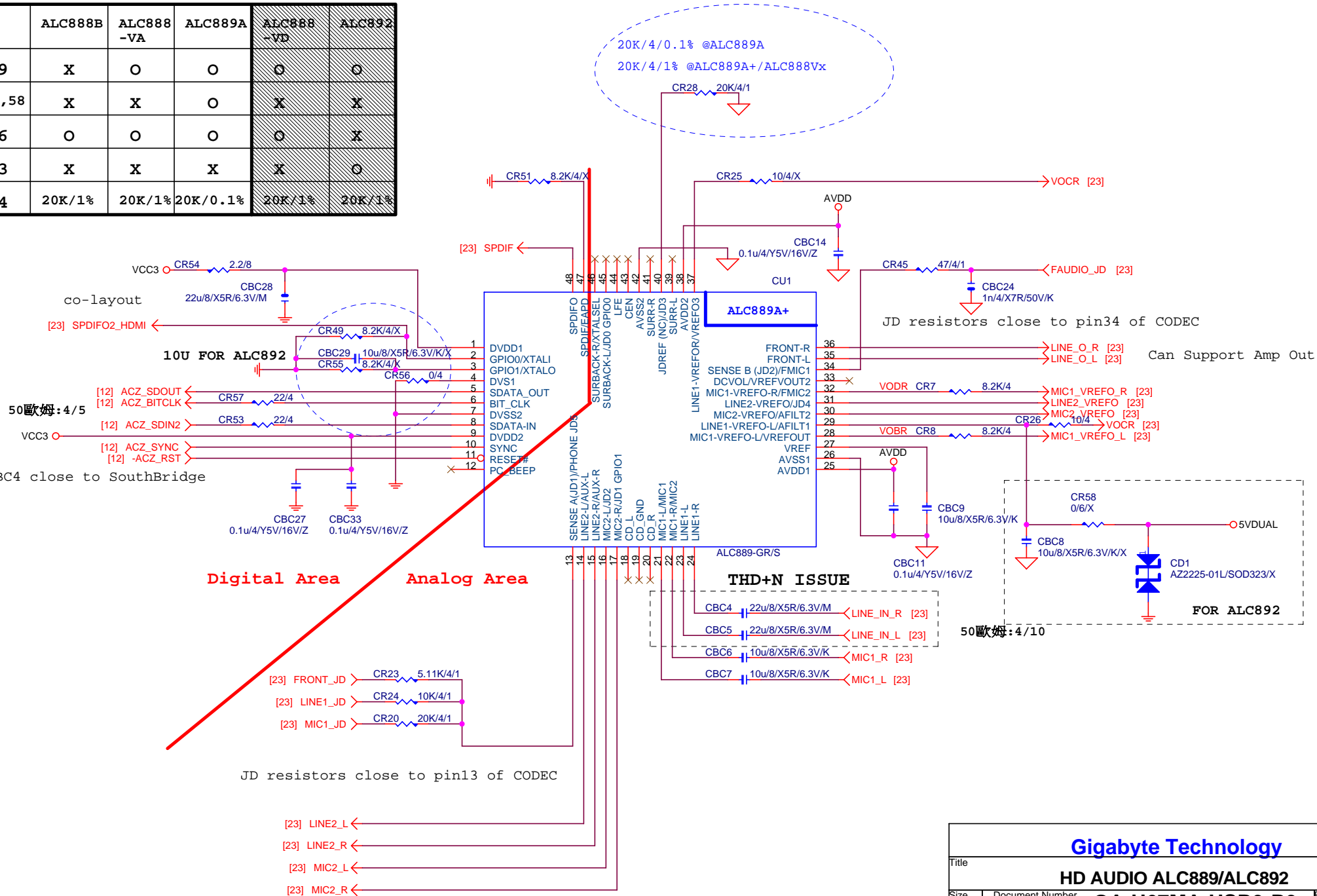
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Document Number			GA-H67MA-USB3-B3
Date:			Wednesday, March 02, 2011
Sheet			19 of 32
Rev			1.0

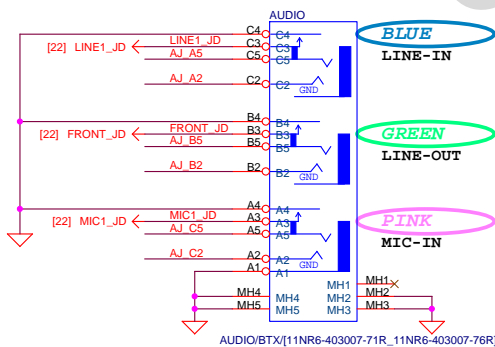
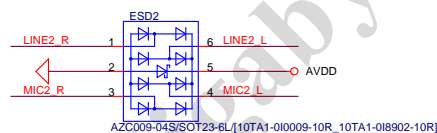
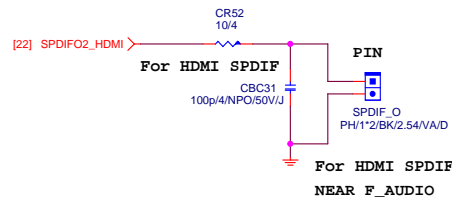
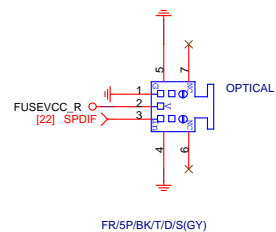


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Only reserved for ALC888

[22] LINE_IN_R ← CR17 62/4

[22] LINE_IN_L ← CR18 62/4

[22] VOCR ← CR3 8.2K/4

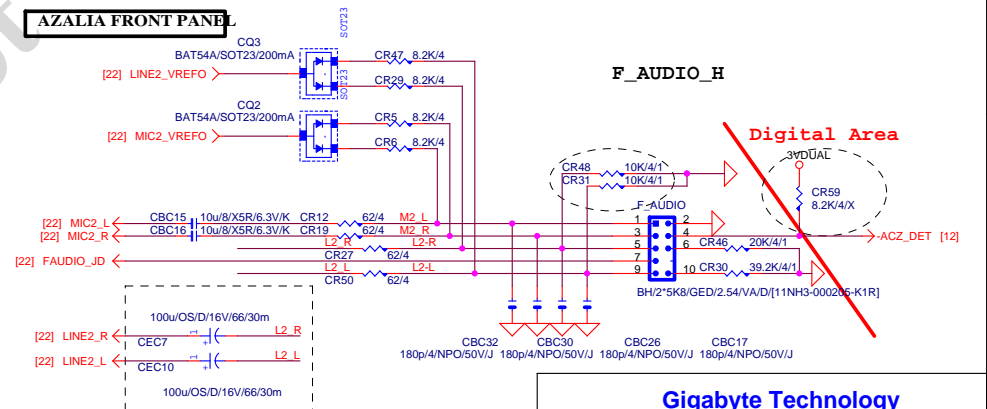
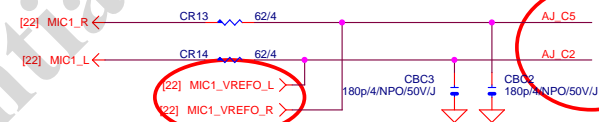
CR4 8.2K/4

CBC13 180pF/NPQ/50V/J

CBC10 180pF/NNP/50V/J

For 889A/888

BAT54A/SOT23/200mA



F_AUDIO_H

Digital Area

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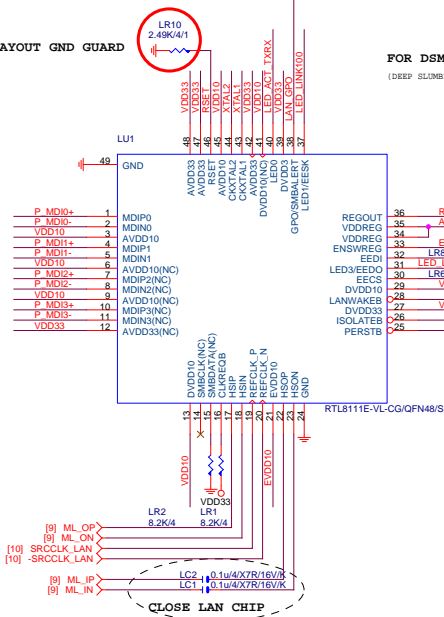
AUDIO JACK

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Date: Wednesday, March 02, 2011	Sheet 23 of 32	

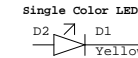
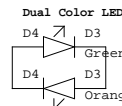
PCIE-1G LAN

XTAL2:外部CLK IN

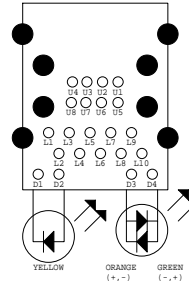
RSET需LAYOUT GND GUARD

FOR DSM MODE
(DEEP SLEEP MODE)

ENABLE SW



P35-152-19W9

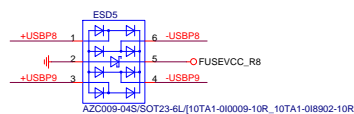


USB_LAN CONNECTOR

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USB_LAN CONNECTOR

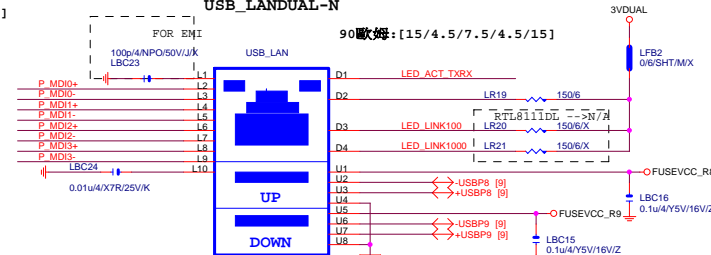
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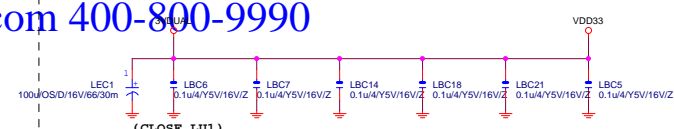
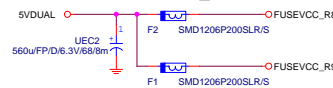
CLOSE USB_LAN

USB_LANDUAL-N

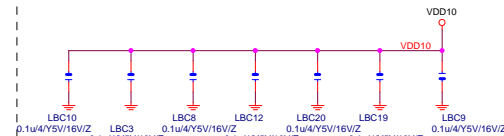
90欧姆:[15/4.5/7.5/4.5/15]

USB+LAN1G/GO.Y/OS/RA/D1/RED[11NR6-702009-11R_11NR6-702009-41R]
RED 11NR6-702009-11R:1G/12CORE
11NR6-702009-41R:1G/8CORE

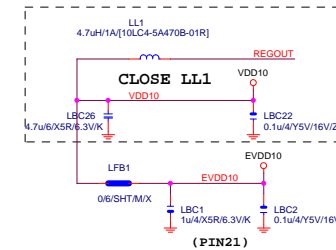
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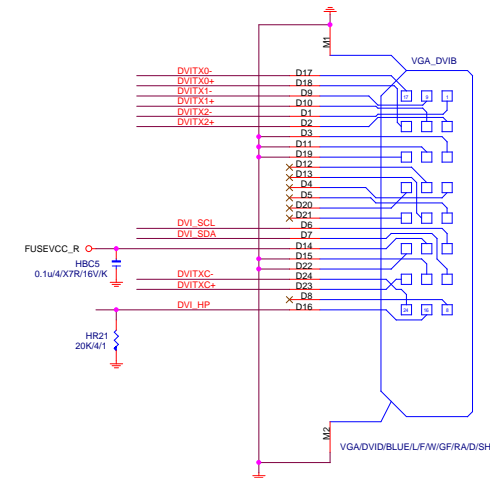
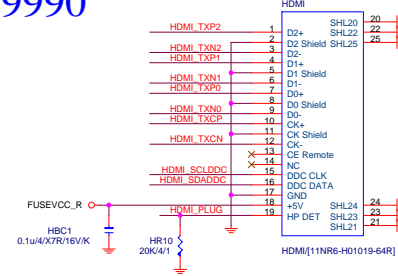
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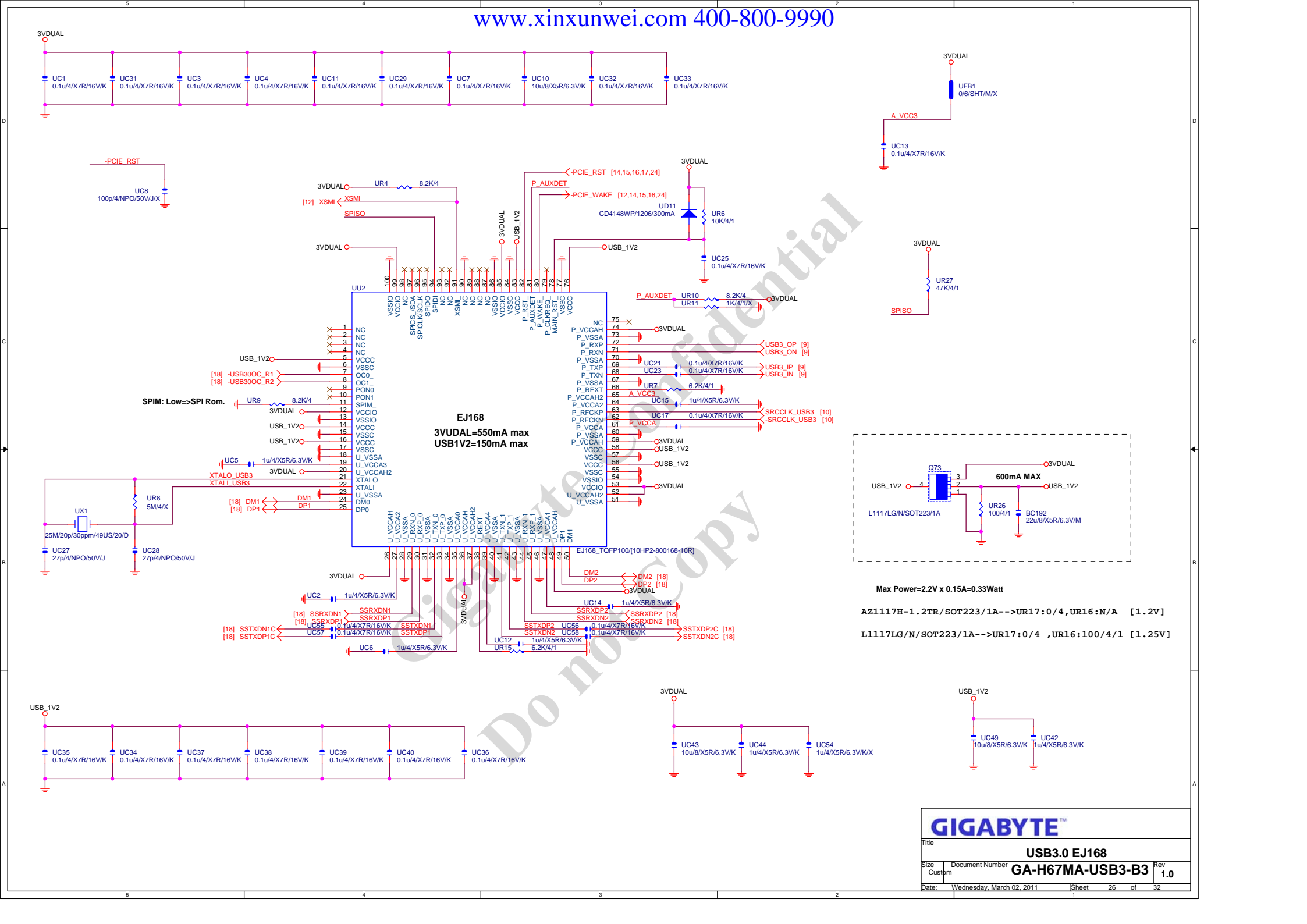


(PIN3, 6, 9, 13, 29, 41, 45)



(PIN21)





The ERP schematic diagram shows a 5V DUAL connection to a resistor network (R449 220/4) and a capacitor (EC15 100uF/OS/D16V/66/30m) connected to a MOSFET (Q68 AP431N/SOT23/150mA).

3VDUAL_PCH

R263 8.2K/4

3VDUAL

Q27 PMBT2907A/SOT23-600mA/50

80T23

3VDUAL_PCH

R265 220/4

3VDUAL

Q26 PMBT2907A/SOT23-600mA/50

80T23

3VDUAL_PCH

R264 220/4

3VDUAL

Q30 MMBT2222A/SOT23/600mA/40

80T23

R266 8.2K/4

SVSB_CTRL

80T23

在ERP TURN ON時，先將PCH 3VDUAL灌入3VDUAL_PCH，使TURN ON -SLP_S3功能

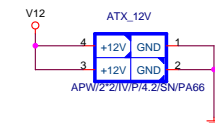
The schematic diagram illustrates the RSMRST signal path. It begins with a 12V input signal, [12] -DEPSLV, which is connected to a 3V3DUAL regulator (Q65, L1085DG/TO252/5A). The output of this regulator is connected to a network of resistors (R439, R451, R434, R345) and capacitors (C128, C127, C126). The signal then passes through a 3V3DUAL regulator (Q42, MMBT2222A/SOT23/600mA/40) and a network of resistors (R334, R343, R344, R345) and capacitors (C127, C128). The final output is a 5VSB signal labeled [12] -RSMRST. A note "Meet the rise time" is present near the output.

The schematic diagram illustrates the power supply section of the TMS320C42 evaluation board. It shows a 2.5LEVEL input connected to a network of resistors (R407, R409) and capacitors (BC174) leading to the VCC1_05_EN pin. A 12V supply is connected to the op-amp (U88 LM358DR/SO8) and the VCC1_05_G pin. The op-amp's output is connected to the VCC1_05_PCH pin. A 15V DDR supply is connected to the VCC1_05_G pin and the VCC1_05_PCH pin. The circuit also includes a 10uF/8VSR/6.3V/K capacitor (BC133) and a 550uF/PPID/6.3V/68mm capacitor (EC9). The output is labeled VCC1_05_PCH.

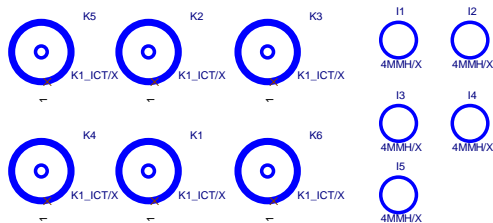
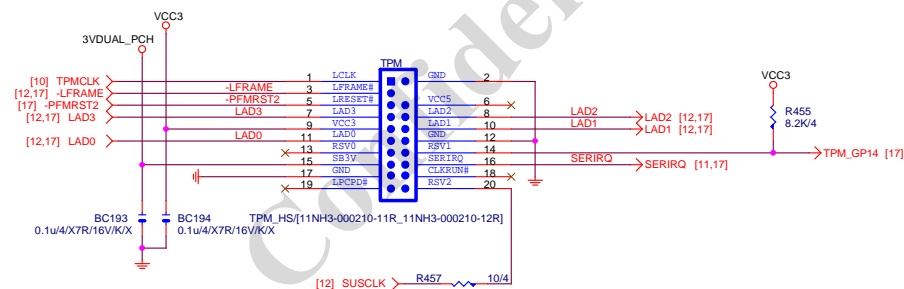
[illegible][illegible][illegible]

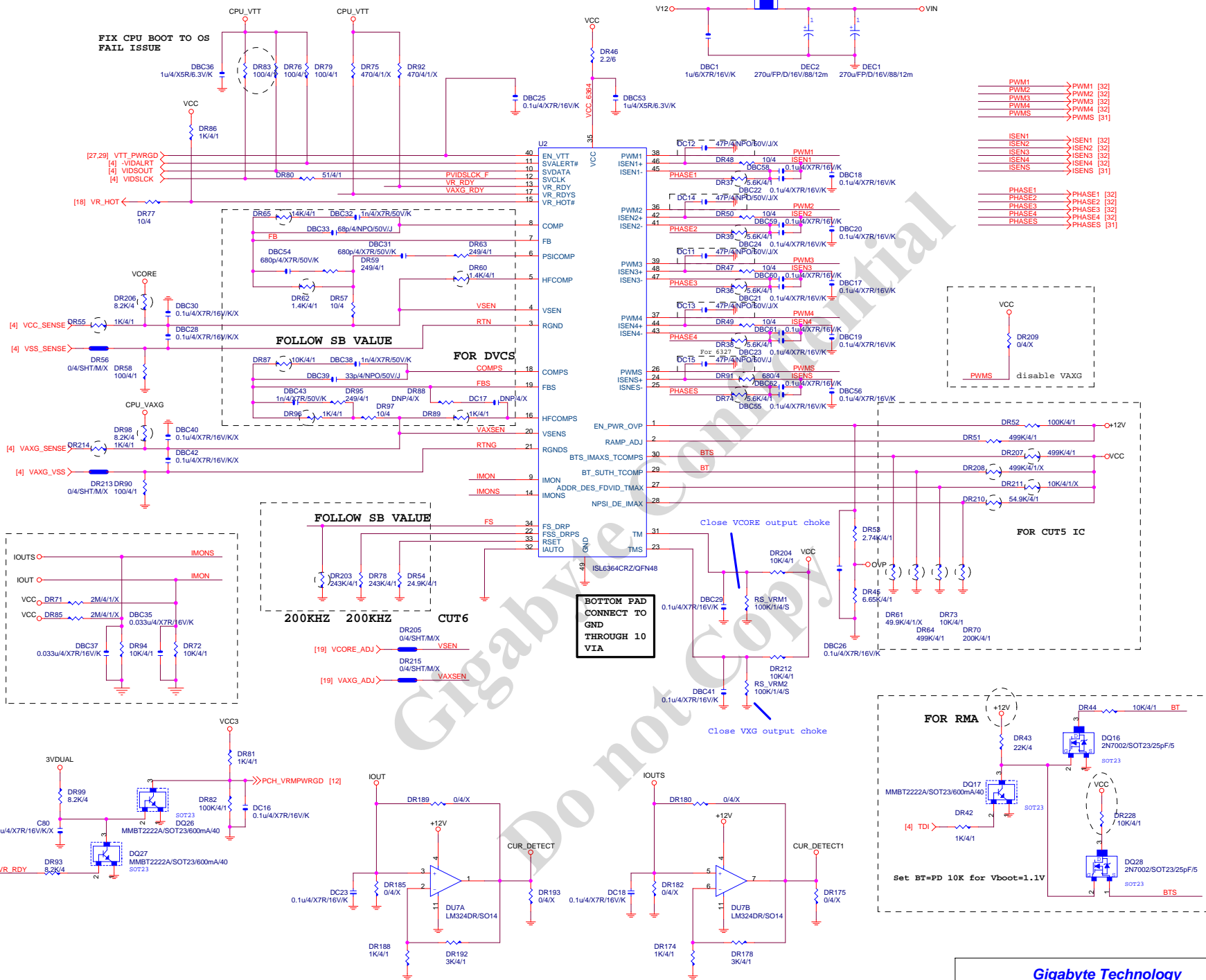
The diagram shows a horizontal beam of total length 3. It is divided into two segments: a left segment of length 1 and a right segment of length 2. A vertical dashed line is positioned at the midpoint of the right segment, indicating the location of a crack. The beam is supported at both ends by vertical lines.

www.xinxunwei.com **ATXX4 POWER CONNECTOR**



ATX_4-5

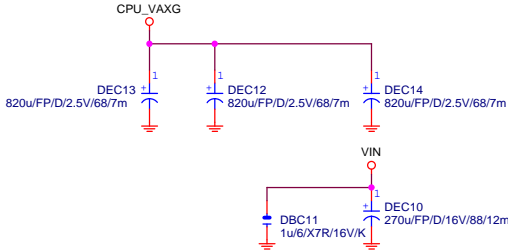
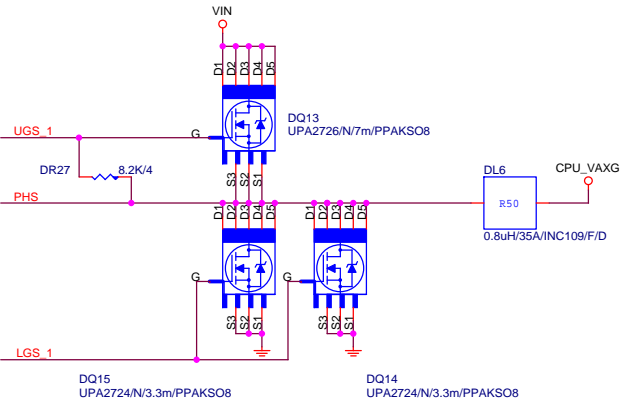
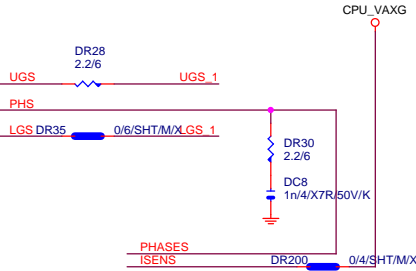
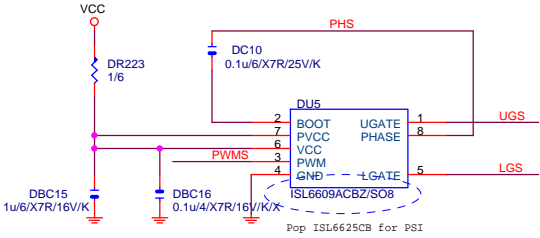




VAXG

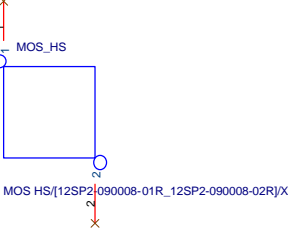
ISL6612 use +12V
ISL6609 use VCC

ISL6612 use 1 ohm
ISL6609 use 0 ohm



MOS HEATSINK

MOSHSINK-P67A-D3



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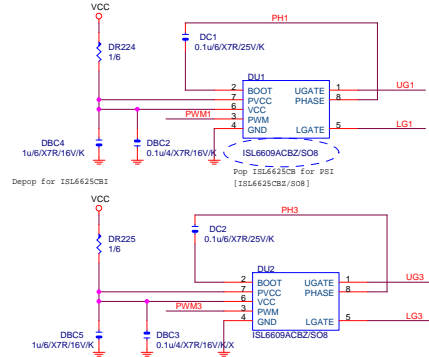
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ISEN1 → ISEN1 [30]
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 ISEN3 → ISEN3 [30]
 ISEN4 → ISEN4 [30]

PHASE1 → PHASE1 [30]
 PHASE2 → PHASE2 [30]
 PHASE3 → PHASE3 [30]
 PHASE4 → PHASE4 [30]

ISL6612 use +12V
 ISL6609 use VCC

ISL6612 use 1 ohm
 ISL6609 use 0 ohm

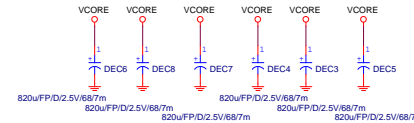
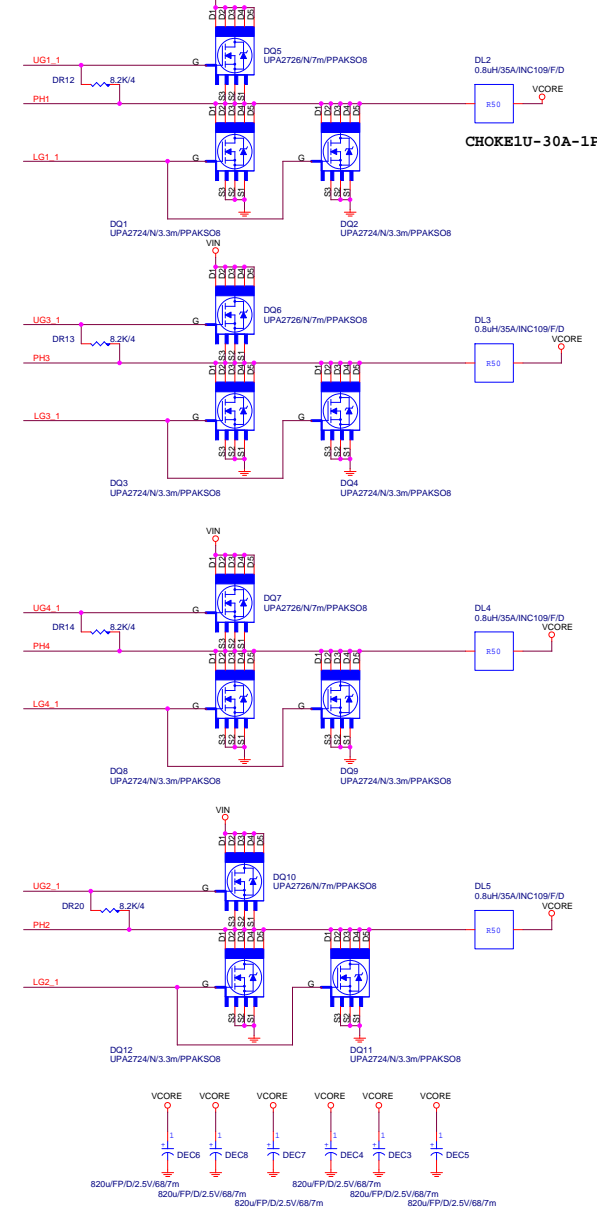
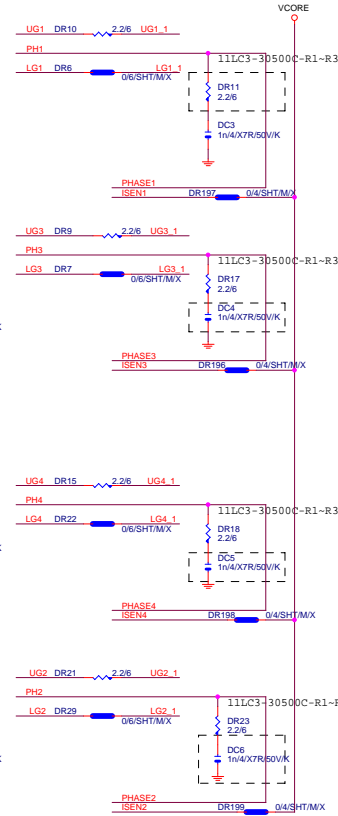


[1]

[3]

[4]

[2]



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